

Fig.1

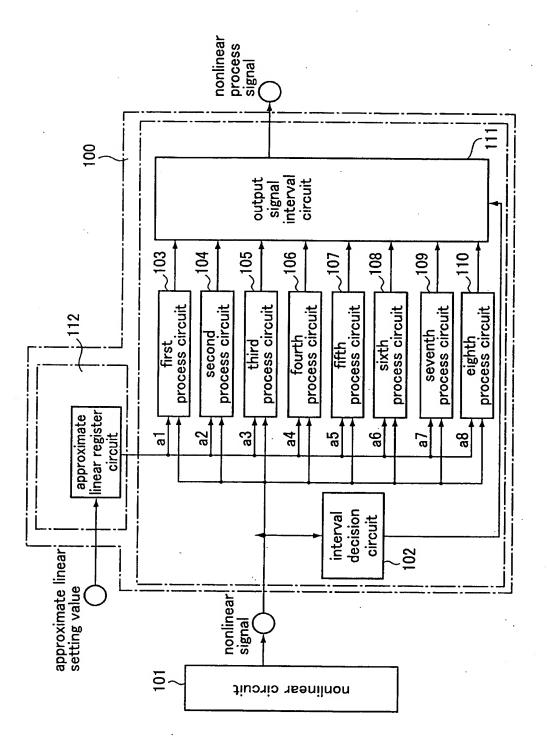


Fig.2

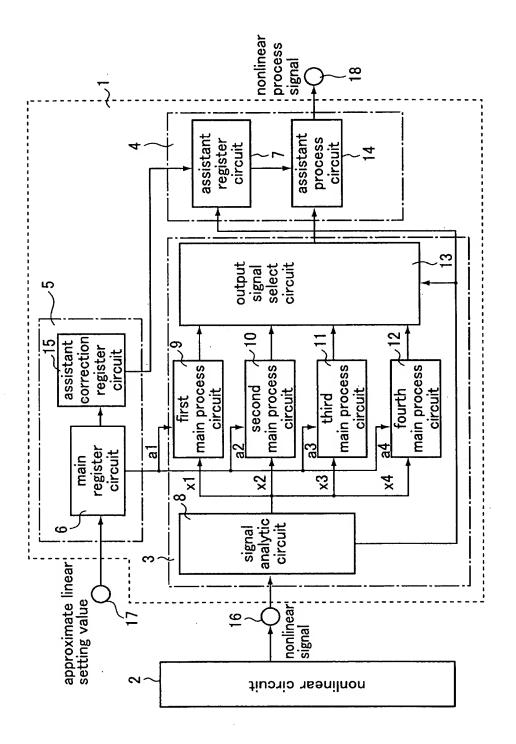


Fig.3

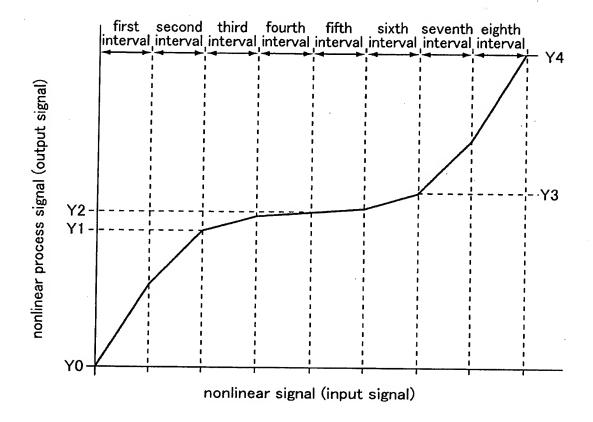


Fig.4

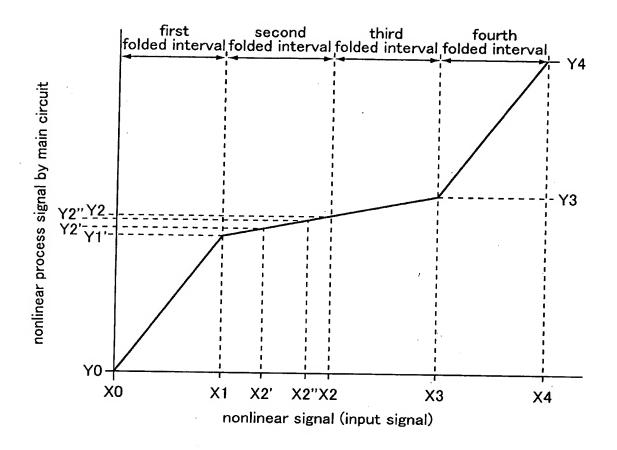


Fig.5

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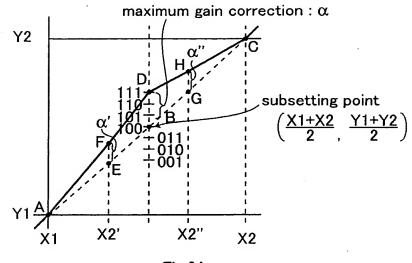


Fig.6A

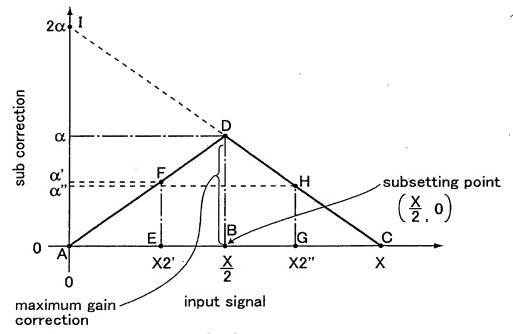


Fig.6B

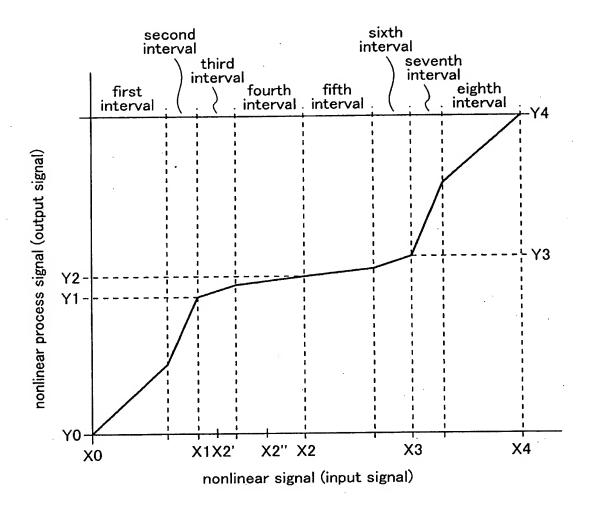


Fig.7

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INV: Yasuyuki MIZUSAKI
SHEET 8 OF 8

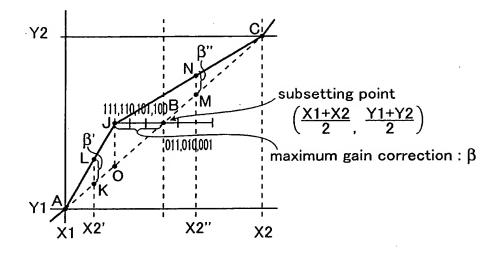
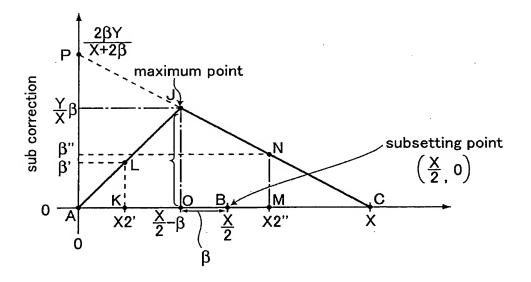


Fig.8A



input signal

Fig.8B